PATENT APPLICATION	SERIAL NO	·
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## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FEE RECORD SHEET

04/23/2004 GWORDOF1 00000031 090458 10709220

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PTO-1556 (5/87)

## Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

59474

Application ID:

10709220

STRUCTURE AND METHOD OF

FORMING BIPOLAR TRANSISTOR

Title of Invention:

HAVING A SELF-ALIGNED RAISED

EXTRINSIC BASE USING SELF-

ALIGNED ETCH STOP LAYER

First Named Inventor:

Marwan Khater

Domestic/Foreign Application:

**Domestic Application** 

Filing Date:

2004-04-22

Effective Receipt Date:

2004-04-22

Submission Type:

**Utility Patent Filing** 

Filing Type:

new-utility

Confirmation number:

3219

Attorney Docket Number:

FIS920030412US1

Total Fees Authorized:

810.0

Payment Category:

**Deposit Account** 

Deposit Account Number:

90458

Deposit Account Name:

H. Daniel Schnurmann

**RAM Payment Status:** 

RAM has not been processed

Digital Certificate Holder: cn=H. Daniel Schnurmann,ou=Registered Attorneys,ou=Patent and

Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US Certificate Message Digest: d9242b4aa2ef2539101301fed0e20ceb42ff8e03

## PATENT APPLICATION FEE DETERMINATION RECORD

Application or Docket Number

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